

WHAT IS CLAIMED:

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1. An endoscope apparatus, comprising:
 - a first drive signal generator portion for generating a first drive signal for driving an imaging device built in or removably connected to an endoscope;
 - a video signal extracting portion for obtaining a first video signal included in a imaging signal obtained in said imaging device;
 - a second drive signal generator portion for generating a second drive signal for controlling a timing when said video signal extracting portion obtains said first video signal from said imaging signal;
 - a first processor for storing at least part of a circuit for obtaining, from said first video signal, a second video signal that can be displayed on a monitor; and
 - a delay circuit, which is stored in said first processor, for delaying at least part of signals among signals included in said first drive signals and said second drive signals.
2. An endoscope apparatus according to claim 1, wherein said first processor is a digital signal processor constructed by an integrated circuit.
3. An endoscope apparatus according to claim 1, wherein said delay circuit is variable in its delay time.
4. An endoscope apparatus according to claim 3, wherein

said delay circuit comprises a multistage buffer circuit connected in series and a circuit for selecting the number of stages of said multistage buffer circuit.

5. An endoscope apparatus according to claim 3, comprising a second processor for setting the delay time of said delay circuit.

6. An endoscope apparatus according to claim 5, comprising:

a switch for specifying said delay time; and
said second processor setting said delay time depending on the condition of said switch.

7. An endoscope apparatus according to claim 5, comprising:

a switch for setting information from which said delay time can be derived ; and

said second processor setting said delay time depending on the condition of said switch.

8. An endoscope apparatus according to claim 7:
wherein information from which said delay time can be derived includes information indicating length of an insert portion of said endoscope.

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9. An endoscope apparatus according to claim 7:
wherein information from which said delay time can be derived includes identification information for identifying a type of said endoscope.

10. An endoscope apparatus according to claim 5,
comprising:

said endoscope including information acknowledgement
portion for giving information indicating said delay time to said
second processor; and

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said second processor setting said delay time depending
on information acknowledged from said information
acknowledgement means.

11. An endoscope apparatus according to claim 5,
comprising:

said endoscope including a information acknowledgement
portion for giving information from which said delay time can
be derived to said second processor; and

said second processor setting said delay time depending
on information acknowledged from said information
acknowledgement means.

12. An endoscope apparatus according to claim 11:
wherein information from which said delay time can be
derived includes information indicating length of a insert
portion of said endoscope.

13. An endoscope apparatus according to claim 11:
wherein information from which said delay time can be
derived includes identification information for identifying a
type of said endoscope.